

Search Notes

Application/Control No.

10/773,232

Examiner

Faisal Zaman

Applicant(s)/Patent under
Reexamination

OKUMURA, YOSHIKI

Art Unit

2112

SEARCHED

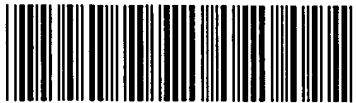
Class	Subclass	Date	Examiner
710	260-269	9/13/2006	FMZ
326	62	9/13/2006	FMZ
713	400-600	9/13/2006	FMZ

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner
710	260-269	9/13/2006	FMZ
326	62	9/13/2006	FMZ
713	400-600	9/13/2006	FMZ

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
Text search focusing on status bit setting circuits in EAST databases: USPAT, USPGPUB, EPO/JPO, DERWENT, IBM_TDB	12/12/2005	FMZ
Checked for Double Patenting & 101 Checked Inventor and Assignee	12/12/2005	FMZ
Consulted SPE Rehana Perveen concerning search strategies	12/12/2005	FMZ
Conducted PLUS search through EIC	12/13/2005	FMZ
Searched Google NPL for status bit setting circuit	12/13/2005	FMZ
Consulted Primary Examiner Paul Myers concerning search strategies	12/13/2005	FMZ
Updated text search in EAST	4/26/2006	FMZ
Consulted SPE Rehana Perveen (AU 2112) concerning Final Rejection	4/27/2006	FMZ

Search Notes (continued)

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**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
Updated text search in EAST	9/8/2006	FMZ
Consulted SPE Rehana Perveen (AU 2112) concerning allowance.	9/12/2006	FMZ